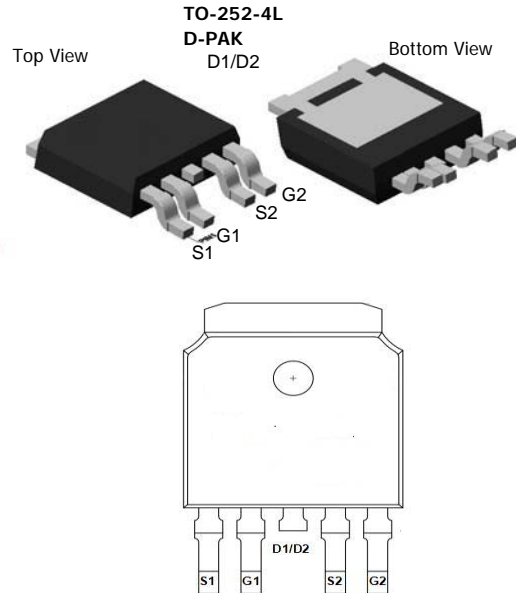


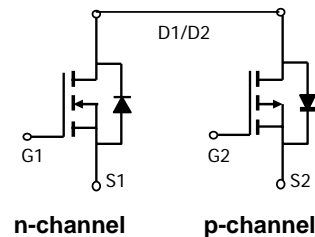
## Features

- N-Channel: 40V, 34A  
 $R_{DS(ON)}$  Typ = 15.4m $\Omega$  @  $V_{GS} = 10V$   
 $R_{DS(ON)}$  Typ = 21m $\Omega$  @  $V_{GS} = 4.5V$
- P-Channel: -40V, -24A  
 $R_{DS(ON)}$  Typ = 28m $\Omega$  @  $V_{GS} = -10V$   
 $R_{DS(ON)}$  Typ = 39.5m $\Omega$  @  $V_{GS} = -4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge



## Application

- Load Switch
- PWM Application
- Power Management



## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Max. N-Channel	Max. P-Channel	Units	
$V_{DSS}$	Drain-Source Voltage	40	-40	V	
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	$\pm 20$	V	
$I_D$	Continuous Drain Current	$T_A = 25^\circ\text{C}$	34	-24	A
		$T_A = 100^\circ\text{C}$	20.4	-14.4	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>	136	-96	A	
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>	33	42	mJ	
$P_D$	Power Dissipation	$T_A = 25^\circ\text{C}$	25	25	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	5	5	$^\circ\text{C}/\text{W}$	
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150		$^\circ\text{C}$	

**N-Channel Electrical Characteristics** ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu\text{A}, V_{GS} = 0\text{V}$	40	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 40\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1.0	1.5	2.0	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(4)</sup>	$V_{GS} = 10\text{V}, I_D = 10\text{A}$	-	15.4	20	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 7\text{A}$	-	21	27.3	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 20\text{V}, f = 1\text{MHz}$	-	1000	-	pF
$C_{oss}$	Output Capacitance		-	84	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	63	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 20\text{V}, I_D = 5\text{A}$	-	14	-	nC
$Q_{gs}$	Gate Source Charge		-	4	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge		-	4.5	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}, V_{DD} = 20\text{V}$ $I_D = 5\text{A}, R_{GEN} = 3\Omega$	-	10	-	ns
$t_r$	Turn-On Rise Time		-	12	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	33	-	ns
$t_f$	Turn-Off Fall Time		-	10	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	34	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	136	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 10\text{A}$	-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = 5\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	19	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	11	-	nC

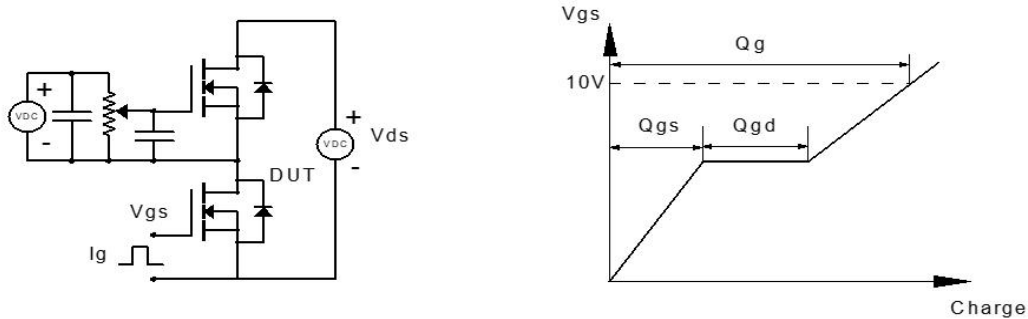
**P-Channel Electrical Characteristics** ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = -250\mu\text{A}, V_{GS} = 0\text{V}$	-40	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = -40\text{V}, V_{GS} = 0\text{V}$	-	-	-1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1.1	-1.6	-2.2	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(4)</sup>	$V_{GS} = -10\text{V}, I_D = -8\text{A}$	-	28	36.4	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}, I_D = -6\text{A}$	-	39.5	51.4	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = -20\text{V}, f = 1\text{MHz}$	-	887	-	pF
$C_{oss}$	Output Capacitance		-	92	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	79	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0 \text{ to } -10\text{V}$ $V_{DS} = -20\text{V}, I_D = -3\text{A}$	-	35	-	nC
$Q_{gs}$	Gate Source Charge		-	6	-	nC
$Q_{gd}$	Gate Drain ("Miller") Charge		-	7	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = -10\text{V}, V_{DD} = -20\text{V}$ $I_D = -5\text{A}, R_{GEN} = 3\Omega$	-	13	-	ns
$t_r$	Turn-On Rise Time		-	10	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	20	-	ns
$t_f$	Turn-Off Fall Time		-	12	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	-24	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-96	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = -8\text{A}$	-	-	-1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = -3\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	23	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	15	-	nC

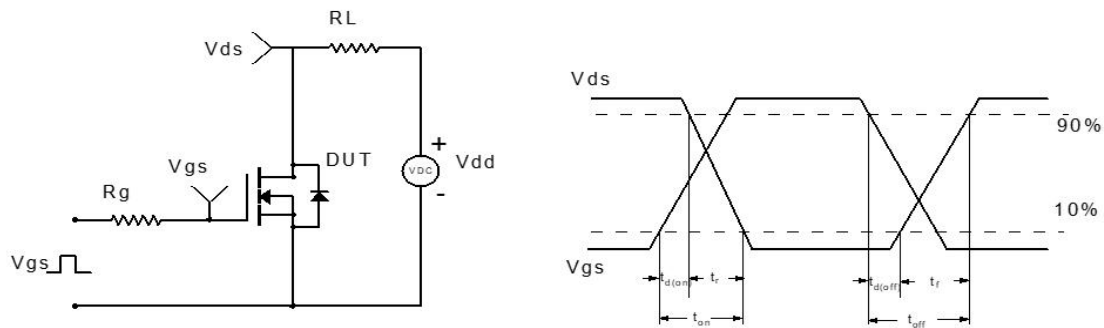
Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2.  $E_{AS}$  condition: Starting  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = 20\text{V}$ ,  $V_G = 10\text{V}$ ,  $R_G = 25\text{ohm}$ ,  $L = 0.5\text{mH}$ ,  $I_{AS} = 11\text{A}$   
 $E_{AS}$  condition: Starting  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = -20\text{V}$ ,  $V_G = -10\text{V}$ ,  $R_G = 25\text{ohm}$ ,  $L = 0.5\text{mH}$ ,  $I_{AS} = -11\text{A}$
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

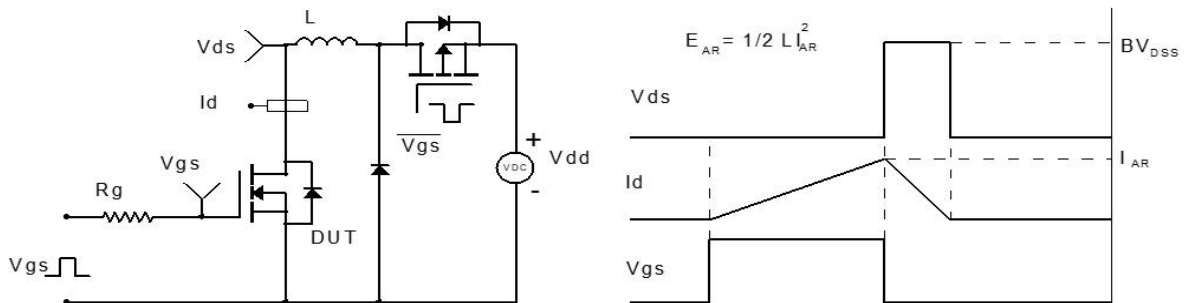
### Test Circuit N-Channel



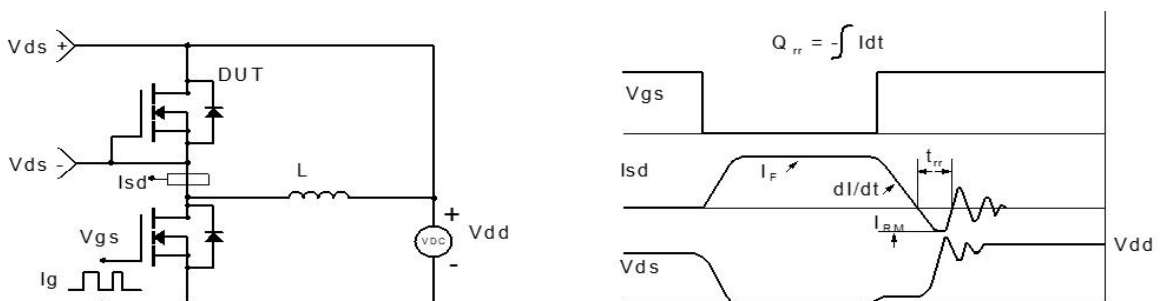
**Figure 1: Gate Charge Test Circuit & Waveform**



**Figure 2: Resistive Switching Test Circuit & Waveform**



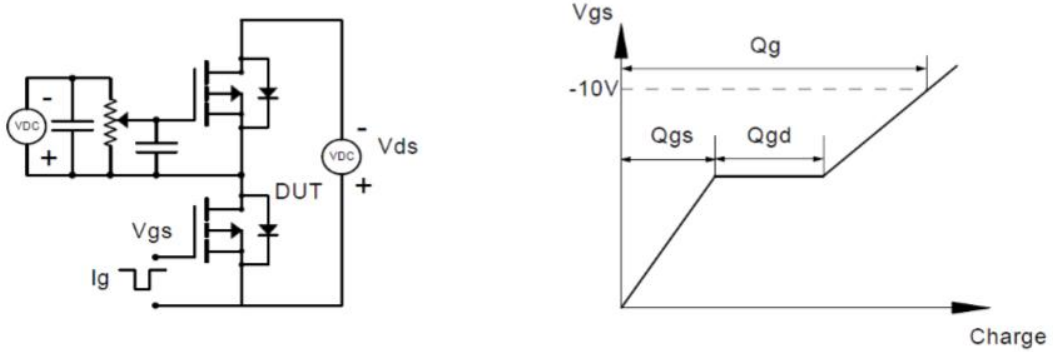
**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**



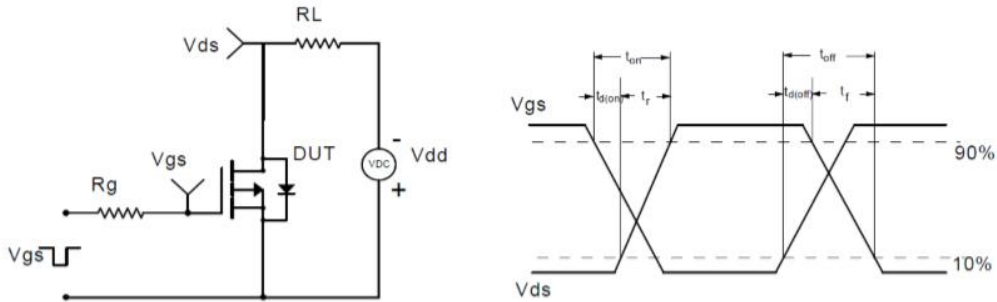
**Figure 4: Diode Recovery Test Circuit & Waveform**

## Test Circuit P-Channel

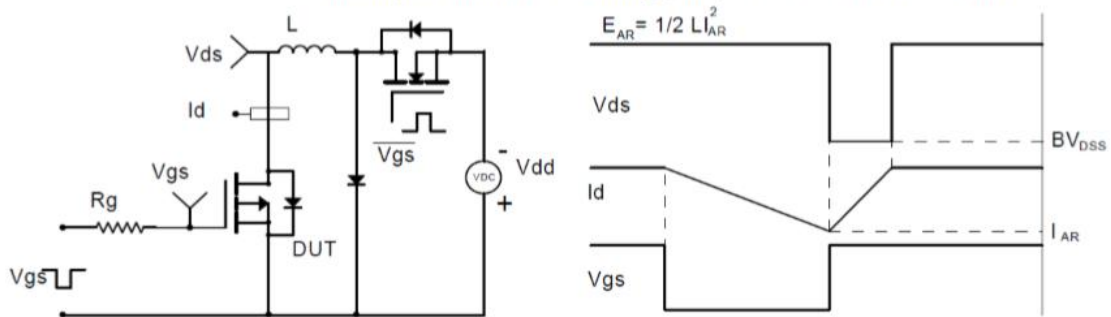
Gate Charge Test Circuit & Waveform



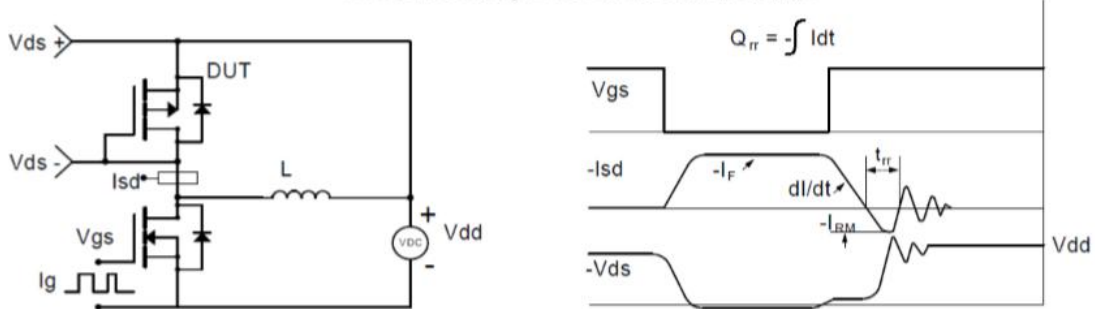
Resistive Switching Test Circuit & Waveforms



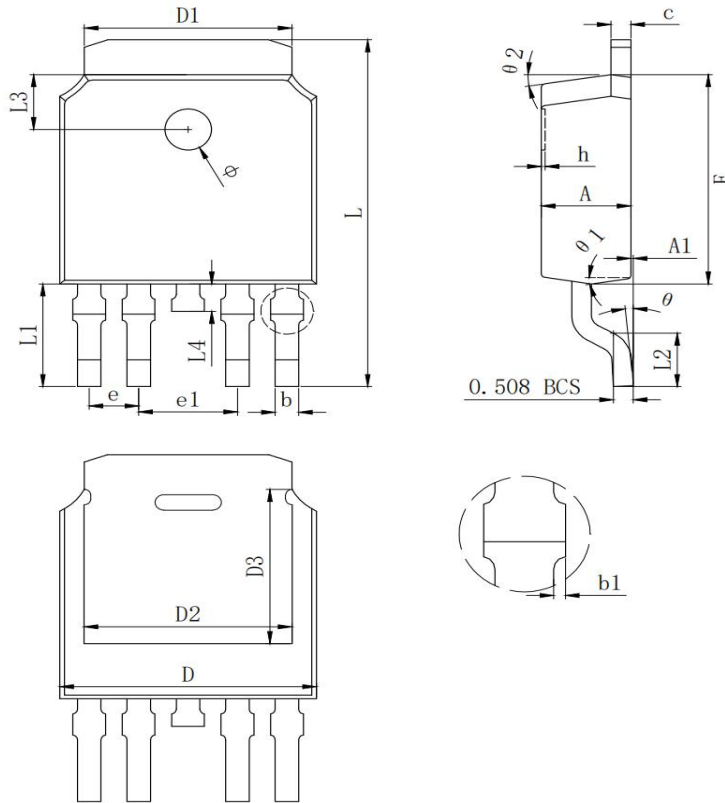
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## Package Mechanical Data(TO-252-4L)



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.550	0.600	0.650
b1	0.000		0.120
c (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	5.346 REF		
D3	4.490 REF		
E	6.000	6.100	6.200
e	1.270 TYP		
e1	2.540 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.988 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.700	0.800	0.900
φ	1.100	1.200	1.300
θ	0°		8°
θ 1	9° TYP		
θ 2	9° TYP		